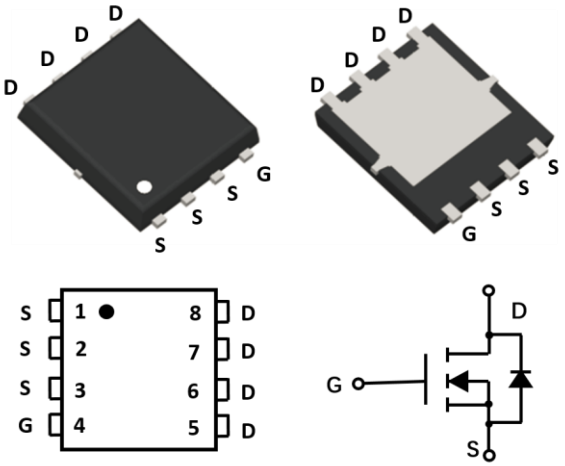
N-Channel Enhancement Mode Field Effect Transistor

PDFN5060-8L



Product Summary

- V_{DS} 60V
- I_D 110A
- I_D (Package limited) 80A
- $R_{DS(ON)}$(at V_{GS}=10V) ＜4.2 mohm
- $R_{DS(ON)}$(at V_{GS}=4.5V) ＜5.2 mohm
- 100% UIS Tested
- 100% ▽V_{DS} Tested

General Description

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$

Applications

- DC-DC Converters
- Power management functions
- Industrial and Motor Drive application

■ Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	60	V
Gate-source Voltage		V_{GS}	±20	V
Drain Current		I_D	110	A
Drain Current [A]	T_C=25℃	I_D	80	A
	T_C=100℃		50	
Pulsed Drain Current [B]		I_{DM}	320	A
Avalanche energy [C]		E_{AS}	250	mJ
Total Power Dissipation	Tc=25℃	P_D	95	W
	Tc=100℃		38	
Thermal Resistance Junction-to-Case		$R_{\theta JC}$	1.47	℃/ W
Junction and Storage Temperature Range		T_J ,T_{STG}	-55～+150	℃

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJG80G06A	F1	YJG80G06A	5000	10000	100000	13“ reel

■ Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Conditions		Min	Typ	Max	Units
Static Parameter							
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V, I_D=250μA		60			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V,V_{GS}=0V	T_J=25℃			1	μA
			T_J=55℃			5	
Gate-Body Leakage Current	I_{GSS}	V_{GS}= ±20V, V_{DS}=0V				±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=250μA		1.2	1.7	2.5	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}= 10V, I_D=40A			3.5	4.2	mΩ
		V_{GS}= 4.5V, I_D=30A			4.2	5.2	
Diode Forward Voltage	V_{SD}	I_S=40A,V_{GS}=0V			0.8	1.2	V
Maximum Body-Diode Continuous Current	I_S					80	A
Dynamic Parameters							
Input Capacitance	C_{iss}	V_{DS}=30V,V_{GS}=0V,f=1MHZ			3267		pF
Output Capacitance	C_{oss}				460		
Reverse Transfer Capacitance	C_{rss}				24		
Switching Parameters							
Total Gate Charge	Q_g	V_{GS}=10V,V_{DS}=30V,I_D=20A			67		nC
Gate-Source Charge	Q_{gs}				12		
Gate-Drain Charge	Q_{gd}				8.5		
Reverse Recovery Chrage	Q_{rr}	I_F=20A, di/dt=500A/us			48		
Reverse Recovery Time	t_{rr}				60		ns
Turn-on Delay Time	$t_{D(on)}$	V_{GS}=10V,V_{DD}=30V,R_L=2.5Ω R_{GEN}=3Ω			15		
Turn-on Rise Time	t_r				8		
Turn-off Delay Time	$t_{D(off)}$				48		
Turn-off fall Time	t_f				13		

A. The maximum current rating is package limited.
B. Pulse Test: Pulse Width≤300us,Duty cycle ≤2%.
C. T_j=25℃, V_{DD}=30V, V_G=10V, L=0.5mH, R_g=25Ω

■ Typical Performance Characteristics

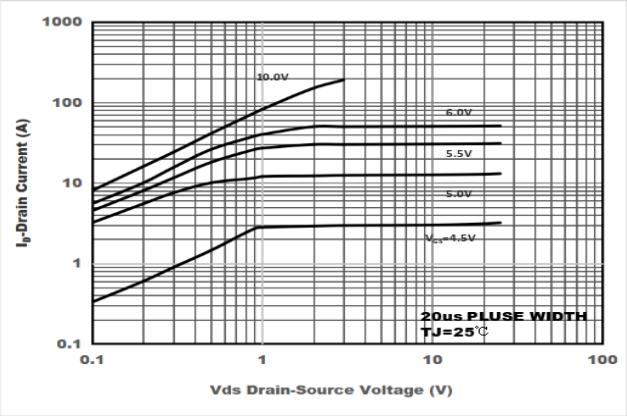


Figure1. Output Characteristics

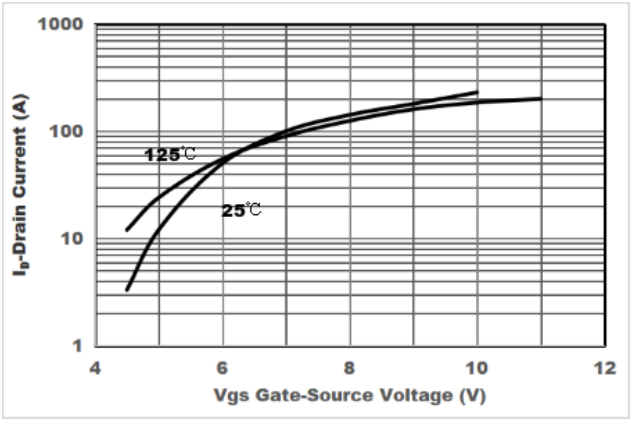


Figure2. Transfer Characteristics

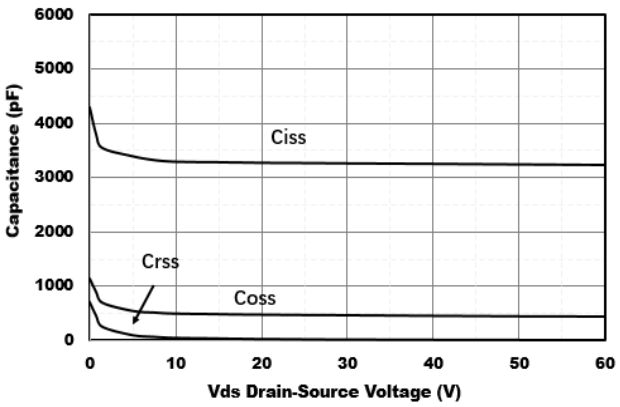


Figure3. Capacitance Characteristics

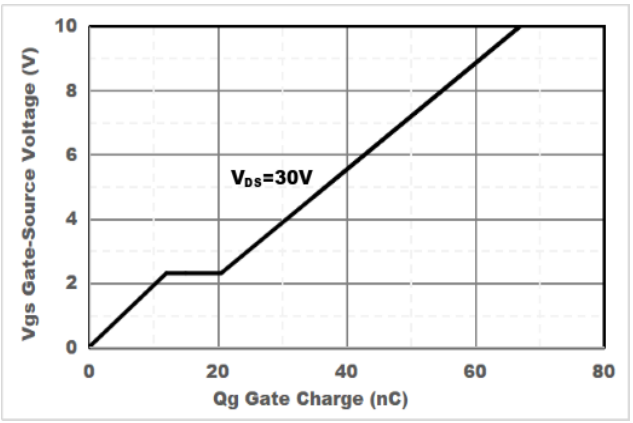


Figure4. Gate Charge

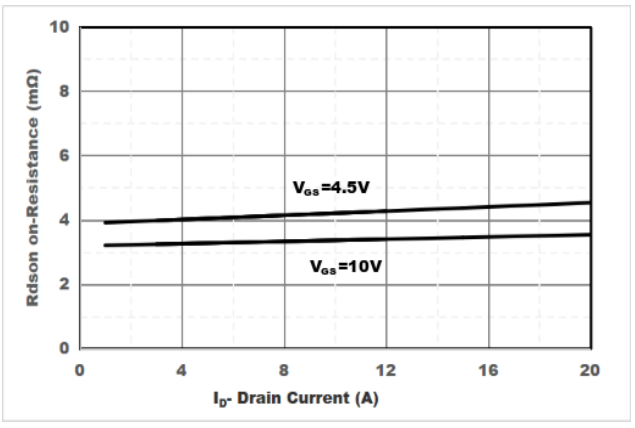


Figure5. Drain-Source on Resistance

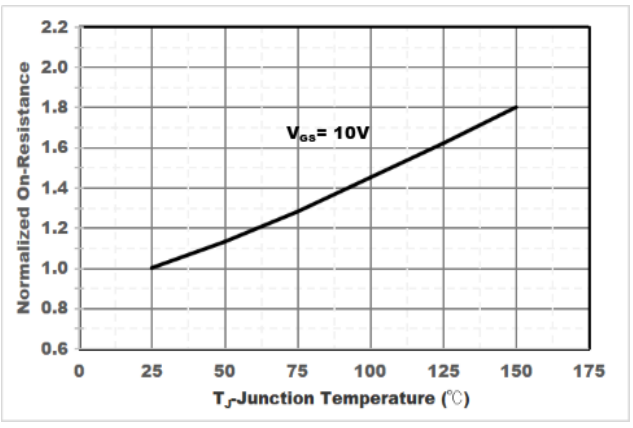


Figure6. Drain-Source on Resistance

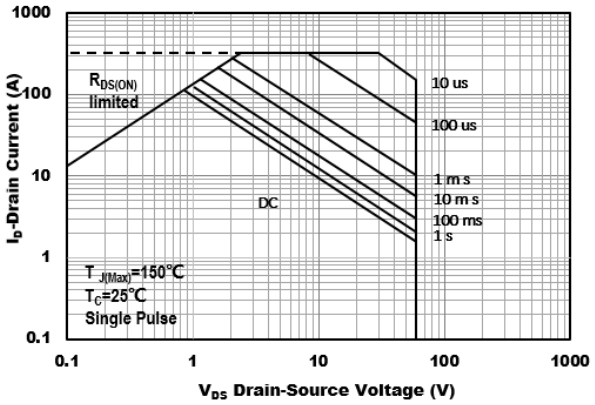


Figure7. Safe Operation Area

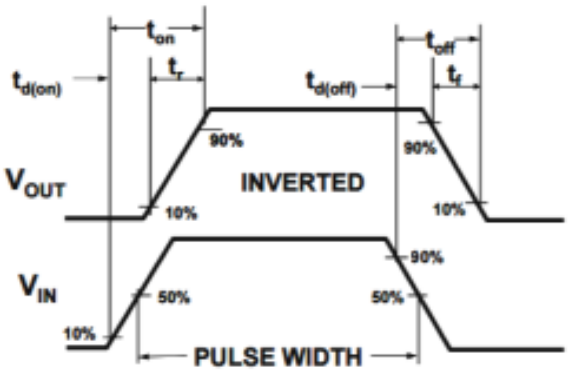
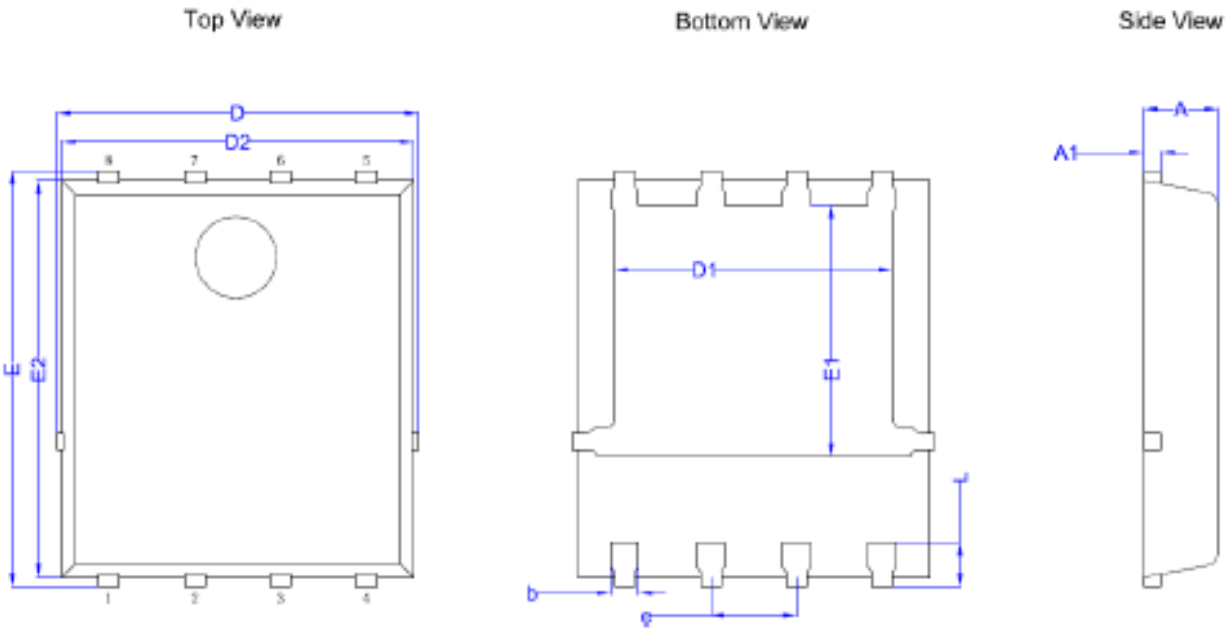


Figure8. Switching wave

■ PDFN5060-8L Package information



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	1.00	1.10	1.20
A1		0.254 BSC	
D	5.15	5.35	5.55
E	5.95	6.15	6.35
D1	3.92	4.12	4.32
E1	3.52	3.72	3.92
D2	5.00	5.20	5.40
E2	5.66	5.86	6.06
e		1.27BSC	
b	0.31	0.41	0.51
L	0.56	0.66	0.76

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