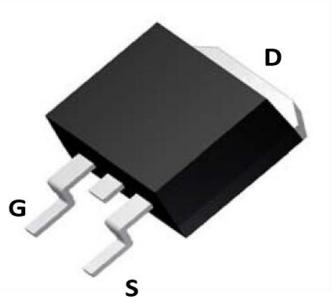
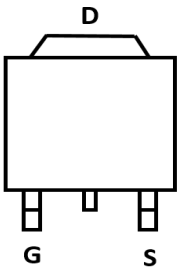
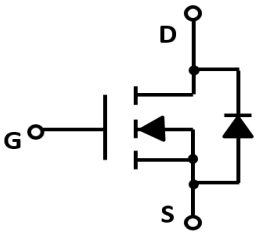
N-Channel Enhancement Mode Field Effect Transistor





TO-263



Product Summary

- V_{DS} 60V
- I_D 200A
- $R_{DS(ON)}$(at V_{GS}=10V) ＜2.6 mohm
- $R_{DS(ON)}$(at V_{GS}=4.5V) ＜3.6 mohm
- 100% UIS Tested
- 100% ▽V_{DS} Tested

General Description

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$

Applications

- Consumer electronic power supply
- Isolated DC-DC Converters
- Motor control
- Invertors

■ Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	60	V
Gate-source Voltage		V_{GS}	±20	V
Drain Current [A]	T_C=25℃	I_D	200	A
	T_C=100℃		125	
Pulsed Drain Current [B]		I_{DM}	600	A
Avalanche energy [C]		E_{AS}	500	mJ
Total Power Dissipation [D]		P_D	260	W
Thermal Resistance Junction-to-Case		$R_{\theta JC}$	0.48	℃/ W
Thermal Resistance Junction-to-Ambient [E]		$R_{\theta JA}$	28	
Junction and Storage Temperature Range		T_J ,T_{STG}	-55～+150	℃

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJB200G06A	F2	YJB200G06A	800	/	8000	13“ reel

■ Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V, I_D=250μA	60			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V,V_{GS}=0V			1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}= ±20V, V_{DS}=0V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=250μA	1.2	1.8	2.2	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}= 10V, I_D=20A		2.35	2.6	mΩ
		V_{GS}= 4.5V, I_D=15A		2.9	3.6	
Diode Forward Voltage	V_{SD}	I_S=20A,V_{GS}=0V			1.2	V
Maximum Body-Diode Continuous Current	I_S				200	A
Dynamic Parameters						
Input Capacitance	C_{iss}	V_{DS}=25V,V_{GS}=0V,f=100KHZ		5950		pF
Output Capacitance	C_{oss}			1250		
Reverse Transfer Capacitance	C_{rss}			85		
Switching Parameters						
Total Gate Charge	Q_g	V_{GS}=10V,V_{DS}=50V,I_D=50A		93		nC
Gate-Source Charge	Q_{gs}			17		
Gate-Drain Charge	Q_{gd}			14		
Reverse Recovery Chrage	Q_{rr}	I_F=25A, di/dt=100A/us		73		
Reverse Recovery Time	t_{rr}			68		ns
Turn-on Delay Time	$t_{d(on)}$	V_{GS}=10V,V_{DD}=30V,I_D=25A R_{GEN}=2Ω		22.5		
Turn-on Rise Time	t_r			6.7		
Turn-off Delay Time	$t_{d(off)}$			80.3		
Turn-off fall Time	t_f			26.9		

Note:

A. The maximum current rating is package limited.
B. Repetitive rating; pulse width limited by max. junction temperature.
C. V_{DD}=50 V, R_G=25 Ω, L=0.5mH, starting T_j=25 ℃.
D. P_D is based on max. junction temperature, using junction-case thermal resistance.
E. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 ℃.

■ Typical Performance Characteristics

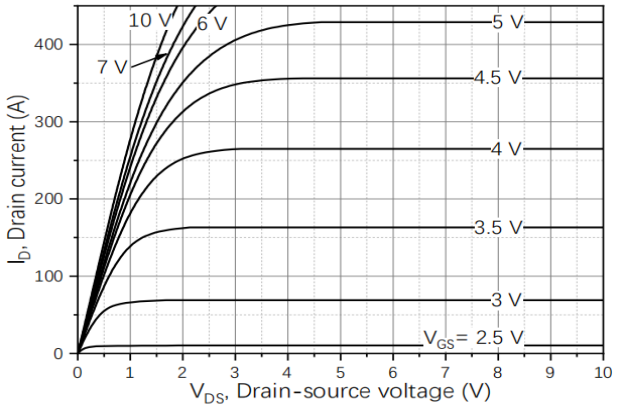


Figure1. Output Characteristics

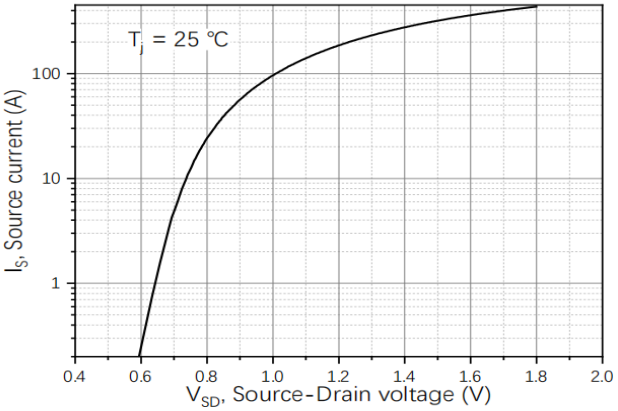


Figure2. Transfer Characteristics

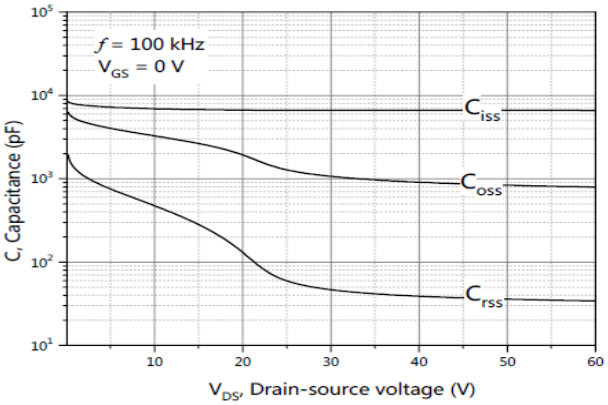


Figure3. Capacitance Characteristics

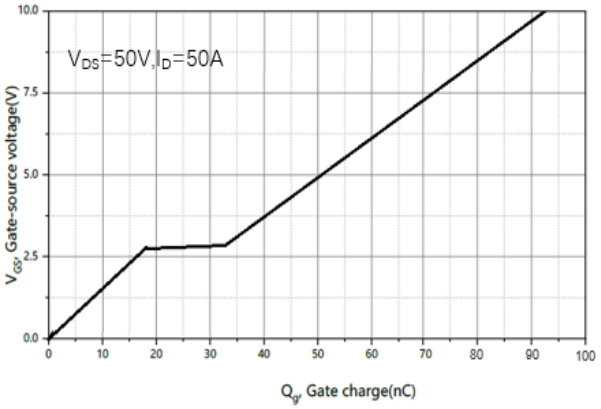


Figure4. Gate Charge

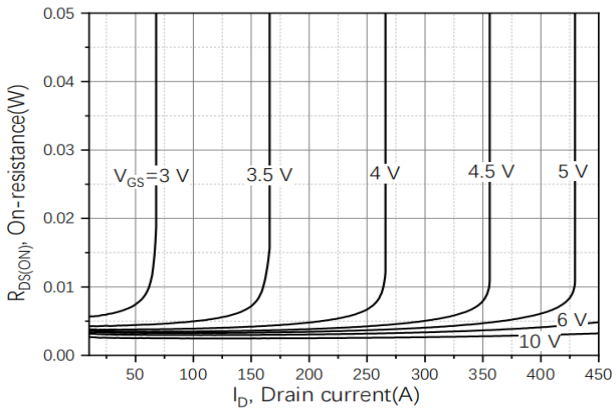


Figure5. Drain-Source on Resistance

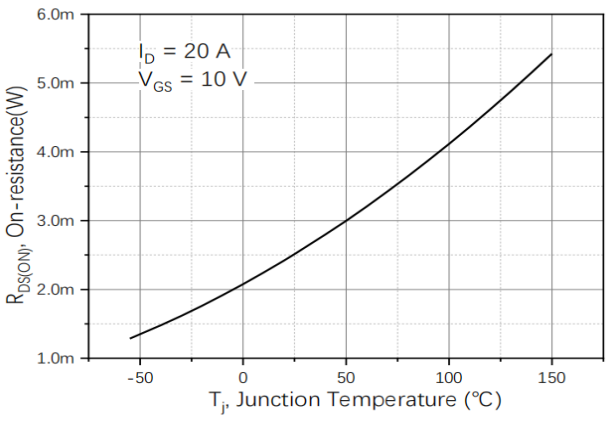


Figure6. Drain-Source on Resistance

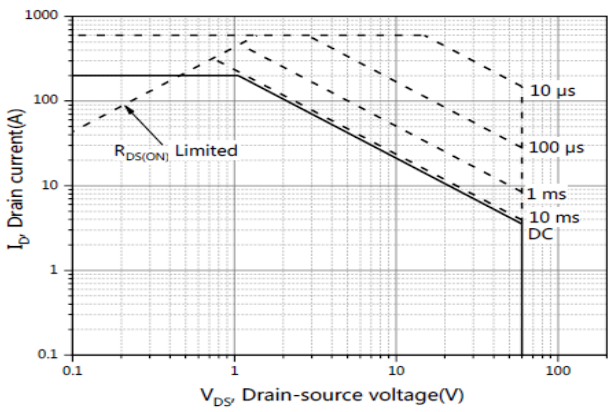


Figure7. Safe Operation Area

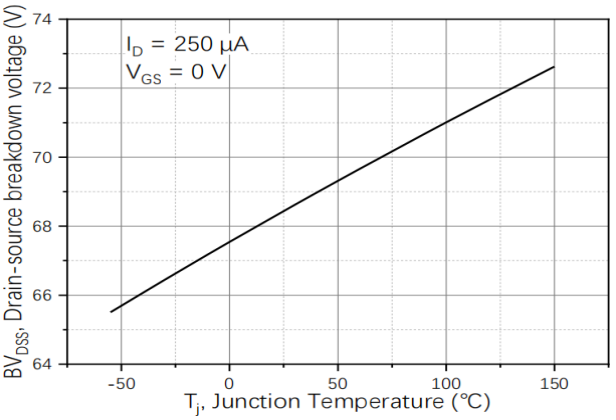


Figure8. Drain-source breakdown voltage

■ Test circuits and waveforms

Figure A: Gate Charge Test Circuit & Waveforms

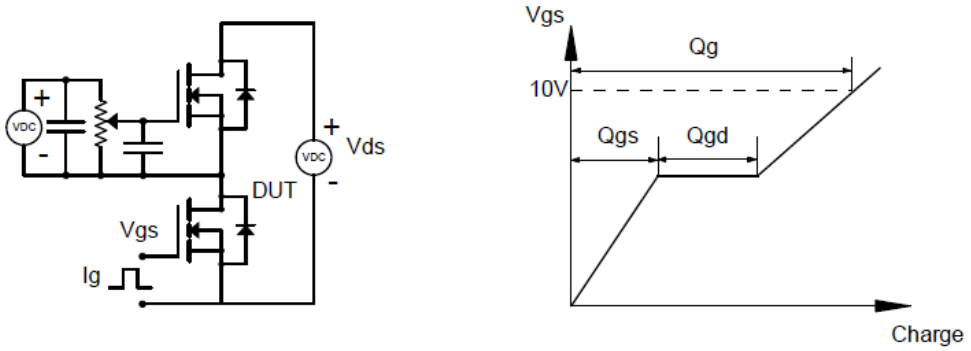


Figure B: Resistive Switching Test Circuit & Waveforms

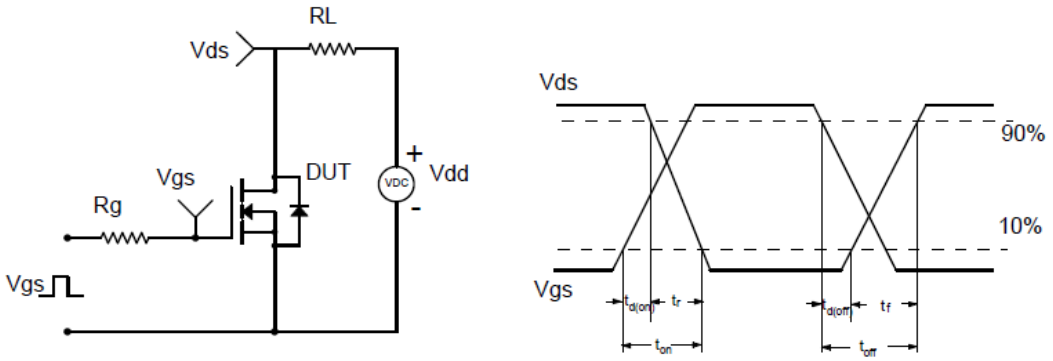


Figure C: Unclamped Inductive Switching (UIS) Test

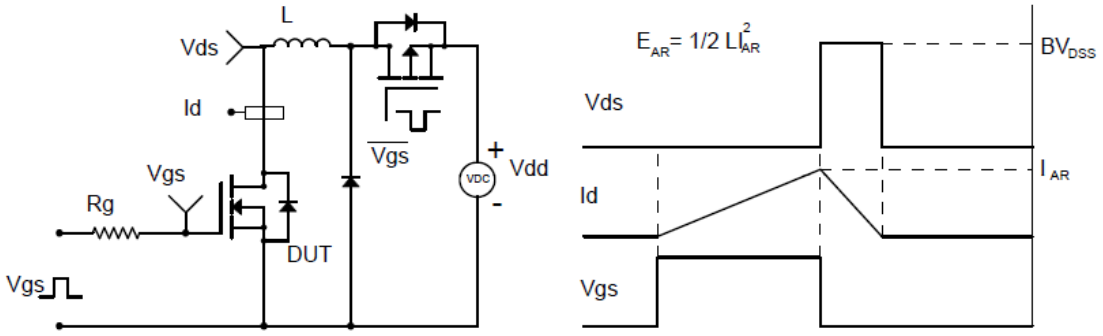
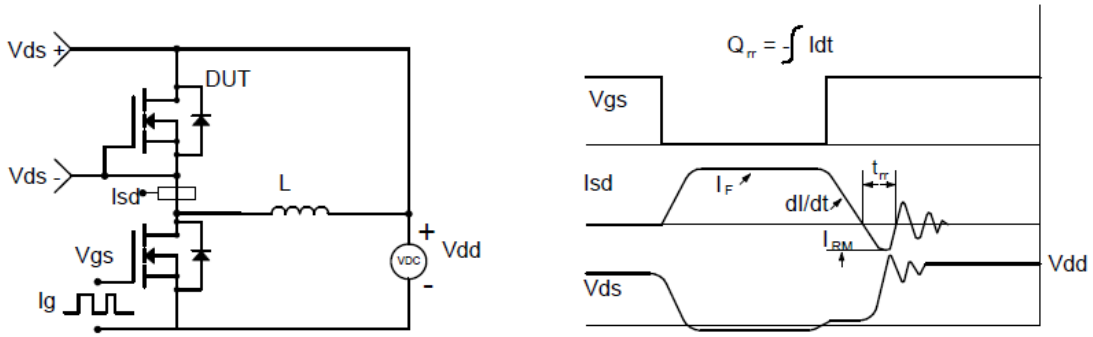
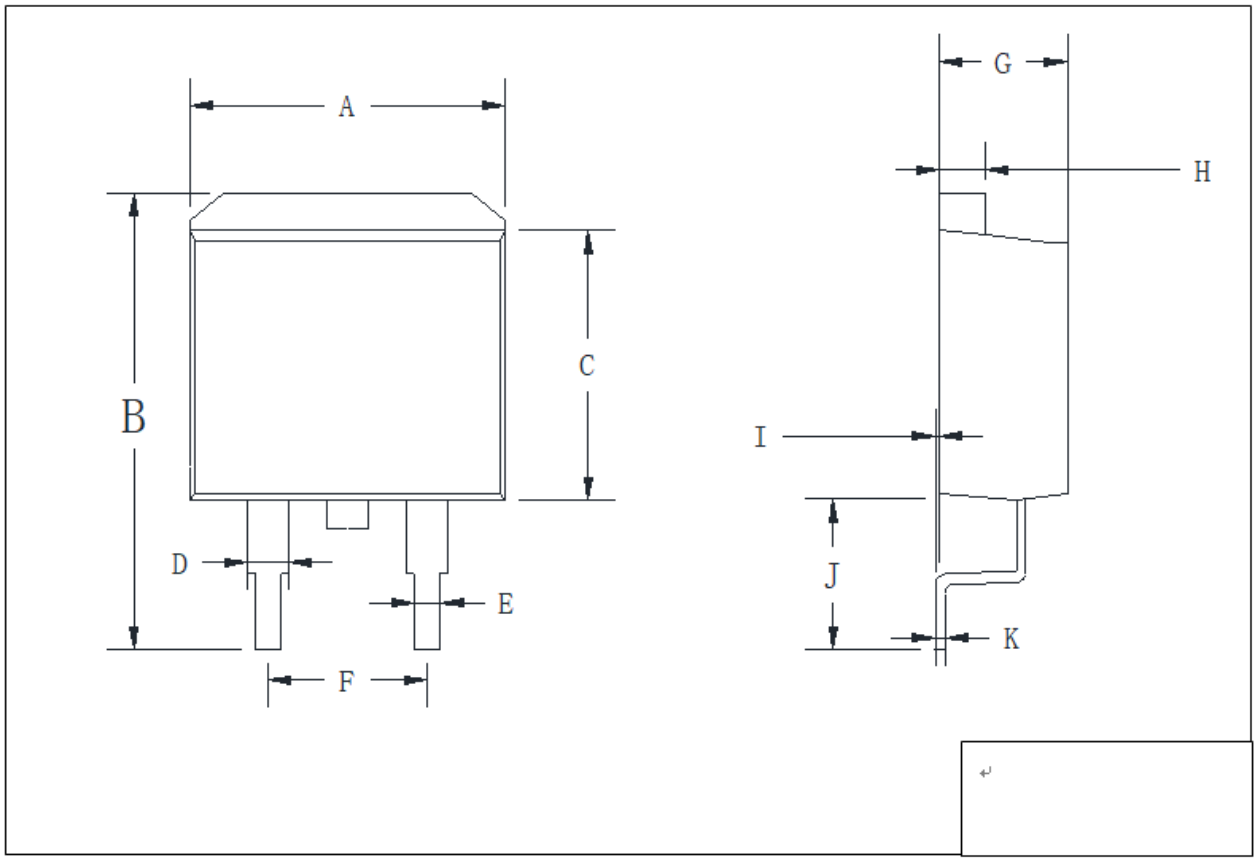


Figure D: Diode Recovery Test Circuit & Waveforms



■ TO-263 Package information



A	B	C	D	E	F
10.15±0.05	15.0±0.15	8.7±0.05	1.28±0.03	0.82±0.03	5.06±0.03
G	H	I	J	K	
4.58±0.05	1.27±0.03	0~0.2	5.0±0.10	0.38±0.03	

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